



PATENT
Docket No.: ACT-280COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2116

Examiner: Chang, Eric

Serial No.: 10/722,636

Filed: November 25, 2003

In re Application of: Plants et al.

For: DELAY LOCKED LOOP FOR AN FPGA ARCHITECTURE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop Issue Fees, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 7/22/05.

Signed Krista Thompson
Krista Thompson

TRANSMITTAL OF ISSUE FEE

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Sir:

Enclosed herewith for filing with respect to the above-identified patent application, please find:

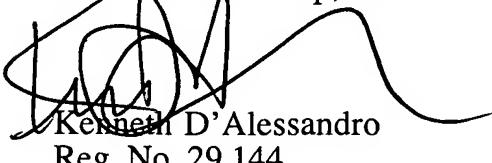
1. (PTOL-85B) Part B - Fees Transmittal (1p)
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In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 50-0612. A duplicate of this transmittal is enclosed.

Dated: 7/22/05

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Respectfully submitted,
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